


<b>Search Notes</b> 	<b>Application/Control No.</b> 10681206	<b>Applicant(s)/Patent Under Reexamination</b> FUJI ET AL.
	<b>Examiner</b> Jacob, Mary C	<b>Art Unit</b> 2123

<b>SEARCHED</b>			
<b>Class</b>	<b>Subclass</b>	<b>Date</b>	<b>Examiner</b>
703	14	3/8/07	MCJ

<b>SEARCH NOTES</b>		
<b>Search Notes</b>	<b>Date</b>	<b>Examiner</b>
Updated Inventor Name Search, Assignee Name Search: East, Google Scholar, IEEE	03/08/07	MCJ
East Search: See search history	03/08/07	MCJ
Google Scholar: "FPGA" logic simulation emulation "bridge circuit"; FPGA logic simulation emulation interface OR bridge "directly connected"	3/8/07	MCJ
IEEE: "simulation accelerator" and "bridge circuit" or "interface circuit"; "emulation accelerator" and "bridge circuit" or "interface circuit"; logic simulation and (interface near user) and (connect??? near direct??) and FPGA	3/8/07	MCJ

<b>INTERFERENCE SEARCH</b>			
<b>Class</b>	<b>Subclass</b>	<b>Date</b>	<b>Examiner</b>